# NSN 5962-01-058-1043

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## **Body Length:**

Between 0.750 inches and 0.795 inches

## **Body Width:**

Between 0.245 inches and 0.300 inches

## **Body Height:**

Between 0.120 inches and 0.195 inches

## **Maximum Power Dissipation Rating:**

650.0 milliwatts

#### **Operating Tempurature Range:**

+0.0/+70.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and monolithic and positive outputs and bipolar and programmable and programmed and w/enable and w/decoded

output and expandable and medium speed and w/disable

#### **Inclosure Material:**

Ceramic and glass

## Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

10 input

## **Case Outline Source And Designator:**

-0-001-ag joint electron device engineering council

## **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

5.5 volts power source

## Time Rating Per Chacteristic:

50.00 nanoseconds propagation delay time, low to high level output

#### Memory Device Type:

Rom

#### Memory Capacity:

Unknown

#### Test Data Document:

28480-1816-0045 standard (includes industry or association standards, individual manufactureer standards, etc.).

## **Terminal Type And Quantity:**

16 printed circuit

#### Shelf Life:

N/a

#### Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

Fiig:

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