NSN 5962-01-059-1413

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Overall Width:

Overall Width: 0.325 inches **Body Length:** Between 0.660 inches and 0.785 inches **Body Width:** Between 0.220 inches and 0.280 inches **Body Height:** Between 0.170 inches and 0.180 inches **Operating Tempurature Range:** +0.0/+70.0 degrees celsius Inclosure Material: Silicon **Inclosure Configuration:** Dual-in-line **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** 4 input **Design Function And Quantity:** 2 gate, and **Case Outline Source And Designator:** To-116 joint electron device engineering council Shelf Life: N/a **Unit Of Measure: Demilitarization:**

Yes - demil/mli

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