## NSN 5962-01-061-1714

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Between 0.660 inches and 0.785 inches

**Body Width:** 

Between 0.220 inches and 0.280 inches

**Body Height:** 

Between 0.140 inches and 0.180 inches

**Maximum Power Dissipation Rating:** 

78.0 milliwatts

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### **Storage Tempurature Range:**

-65.0/+150.0 degrees celsius

#### **End Application:**

Data analysis @central type an/gyk-t1

#### **Features Provided:**

Hermetically sealed and monolithic and positive outputs and low power and schottky and resettable

## **Inclosure Material:**

Ceramic and glass

## **Inclosure Configuration:**

Dual-in-line

## **Output Logic Form:**

Transistor-transistor logic

#### **Input Circuit Pattern:**

4 input

## **Design Function And Quantity:**

1 counter, binary

## **Case Outline Source And Designator:**

T0-116 joint electron device engineering council

#### **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 7.0 volts power source

## **Time Rating Per Chacteristic:**

70.00 nanoseconds propagation delay time, low to high level output and 70.00 nanoseconds propagation delay time, high to low level output

## **Test Data Document:**

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

## **Terminal Type And Quantity:**

14 printed circuit

## Shelf Life:

N/a

## **Unit Of Measure:**

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Demilitarization:

Yes - demil/mli

Fiig:

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