NSN 5962-01-063-4714

Memory Microcircuit - Page 1 of 1



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Body Length:

Between 0.755 inches and 0.785 inches

Body Width:

Between 0.245 inches and 0.280 inches

Body Height:

0.180 inches

Operating Tempurature Range:

+0.0/+70.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and programmable and programmed and w/open collector

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

6 input

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

Time Rating Per Chacteristic:

45.00 nanoseconds propagation delay time, low to high level output and 45.00 nanoseconds propagation delay time, high to low level output

Memory Device Type:

Rom

Terminal Type And Quantity:

16 printed circuit

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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