# NSN 5962-01-064-1318

Digital Microcircuit - Page 1 of 1



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## **Body Length:**

Between 0.755 inches and 0.785 inches

## Body Width:

Between 0.245 inches and 0.280 inches

## **Body Height:**

0.180 inches

## **Maximum Power Dissipation Rating:**

2.0 milliwatts

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **Features Provided:**

Hermetically sealed and low power and schottky and positive outputs

#### Inclosure Material:

Ceramic

## Inclosure Configuration:

Dual-in-line

## **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

Dual 5 input

## **Design Function And Quantity:**

2 flip-flop, j-k

## Time Rating Per Chacteristic:

20.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level output

## Shelf Life:

N/a

## Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

#### Fiig:

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