# NSN 5962-01-064-1319

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# **Body Length:**

Between 0.755 inches and 0.785 inches

### **Body Width:**

0.280 inches

# **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

### **Features Provided:**

Hermetically sealed

#### **Inclosure Material:**

Ceramic

# Inclosure Configuration:

Dual-in-line

#### Output Logic Form:

Transistor-transistor logic

#### Input Circuit Pattern:

Quad 2 input

# **Design Function And Quantity:**

1 gate, nand

Shelf Life:

#### N/a

Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

#### Fiig:

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