NSN 5962-01-065-1496

Digital Microcircuit - Page 1 of 1



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Features	Dras	idad.
reatures	PIO	/iaea:

Hermetically sealed and programmed

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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