## NSN 5962-01-068-3310

Memory Microcircuit - Page 1 of 1



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Body Length:
Between 0.747 inches and 0.815 inches
Body Width:
Between 0.220 inches and 0.310 inches
Body Height:
0.185 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Programmed and hermetically sealed and 3-state output
Inclosure Material:
Ceramic or glass or metal
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
10 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
7.0 volts power source
Time Rating Per Chacteristic:
70.00 nanoseconds propagation delay time, low to high level output
Memory Device Type:
Rom
Test Data Document:
30003-247as-c2520 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing
etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
16 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:

Yes - demil/mli

Fiig:

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