NSN 5962-01-071-3317

Memory Microcircuit - Page 1 of 1



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Features Provided:

Programmable and programmed and monolithic and w/decoded output

Inclosure Configuration:

Dual-in-line

Terminal Surface Treatment:

Solder

Memory Device Type:

Rom

Test Data Document:

50804-015505 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

16 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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