NSN 5962-01-075-7602

Memory Microcircuit - Page 1 of 1



view Online at https://aerobasegroup.com/nsn/5962-01-075-7602
Body Length:
Between 0.850 inches and 0.930 inches
Body Width:
Between 0.240 inches and 0.295 inches
Body Height:
Between 0.160 inches and 0.180 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic and positive outputs and high speed and bipolar and programmed and w/decoded output and 3-stat
output
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
12 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.5 volts power source
Time Rating Per Chacteristic:
85.00 nanoseconds propagation delay time, low to high level output and 85.00 nanoseconds propagation delay time, high to low leve
output
Memory Device Type:
Rom
Test Data Document:
82577-932883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
18 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli

Fiig:

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