# NSN 5962-01-075-7605

Memory Microcircuit - Page 1 of 1



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# **Body Length:**

0.440 inches

# **Body Width:**

Between 0.240 inches and 0.325 inches

# **Body Height:**

0.085 inches

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

# Storage Tempurature Range:

-65.0/+150.0 degrees celsius

# **Features Provided:**

Hermetically sealed and monolithic and positive outputs and w/storage and programmable and programmed and schottky and bipolar and

# 3-state output

Inclosure Material:

#### Ceramic and glass

#### Inclosure Configuration:

Flat pack

# **Output Logic Form:**

Transistor-transistor logic

## **Input Circuit Pattern:**

10 input

#### **Terminal Surface Treatment:**

Solder

#### Voltage Rating And Type Per Characteristic:

5.5 volts power source

# **Time Rating Per Chacteristic:**

5.00 nanoseconds propagation delay time, low to high level output and 35.00 nanoseconds propagation delay time, low to high level output and 10.00 nanoseconds propagation delay time, high to low level output and 89.00 nanoseconds propagation delay time, high to low level output

#### Memory Device Type:

Rom

# Test Data Document:

B2577-932820 standard (includes industry or association standards, individual manufactureer standards, etc.).

# Terminal Type And Quantity:

16 flat leads

#### Shelf Life:

N/a

# Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

#### Fiig:

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