## NSN 5962-01-075-9316

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| Body Length:                                                                                                                           |
|----------------------------------------------------------------------------------------------------------------------------------------|
| Between 0.660 inches and 0.785 inches                                                                                                  |
| Body Width:                                                                                                                            |
| Between 0.220 inches and 0.280 inches                                                                                                  |
| Body Height:                                                                                                                           |
| Between 0.140 inches and 0.180 inches                                                                                                  |
| Maximum Power Dissipation Rating:                                                                                                      |
| 700.0 milliwatts                                                                                                                       |
| Operating Tempurature Range:                                                                                                           |
| -55.0/+125.0 degrees celsius                                                                                                           |
| Storage Tempurature Range:                                                                                                             |
| -65.0/+150.0 degrees celsius                                                                                                           |
| Features Provided:                                                                                                                     |
| Hermetically sealed and monolithic and positive outputs and high current and w/totem pole output                                       |
| Inclosure Material:                                                                                                                    |
| Ceramic and glass                                                                                                                      |
| Inclosure Configuration:                                                                                                               |
| Dual-in-line                                                                                                                           |
| Output Logic Form:                                                                                                                     |
| Transistor-transistor logic                                                                                                            |
| Input Circuit Pattern:                                                                                                                 |
| Quad 2 input                                                                                                                           |
| Design Function And Quantity:                                                                                                          |
| 1 driver, high current                                                                                                                 |
| Case Outline Source And Designator:                                                                                                    |
| T0-116 joint electron device engineering council                                                                                       |
| Terminal Surface Treatment:                                                                                                            |
| Solder                                                                                                                                 |
| Voltage Rating And Type Per Characteristic:                                                                                            |
| 5.5 volts power source                                                                                                                 |
| Time Rating Per Chacteristic:                                                                                                          |
| 500.00 nanoseconds propagation delay time, low to high level output and 750.00 nanoseconds propagation delay time, high to low level   |
| output                                                                                                                                 |
| Test Data Document:                                                                                                                    |
| 06481-971085 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc. |
| excludes any specification, standard or other document that may be referenced in a basic governing drawing)                            |
| Terminal Type And Quantity:                                                                                                            |
| 14 printed circuit                                                                                                                     |
| Shelf Life:                                                                                                                            |
| N/a                                                                                                                                    |
| Unit Of Measure:                                                                                                                       |

Yes - demil/mli

Demilitarization:

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