# NSN 5962-01-078-0606

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# **Body Length:**

Between 0.930 inches and 0.975 inches

# **Body Width:**

Between 0.245 inches and 0.300 inches

# **Body Height:**

Between 0.140 inches and 0.180 inches

# **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

# Storage Tempurature Range:

-65.0/+150.0 degrees celsius

# **Features Provided:**

Hermetically sealed and monolithic and positive outputs and edge triggered and w/enable and w/buffered output and low power and schottky and 3-state output and medium speed and w/totem pole output

# and 3-state output and medium speed and whotem por

Inclosure Material:

#### Ceramic and glass

Inclosure Configuration:

Dual-in-line

# **Output Logic Form:**

Transistor-transistor logic

# Input Circuit Pattern:

10 input

# **Design Function And Quantity:**

8 flip-flop, d-type

# **Time Rating Per Chacteristic:**

28.00 nanoseconds propagation delay time, low to high level output and 28.00 nanoseconds propagation delay time, high to low level output

# **Test Data Document:**

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

Shelf Life:

N/a

# Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

Fiig:

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