# NSN 5962-01-078-1814

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Between 0.750 inches and 0.795 inches

**Body Width:** 

Between 0.245 inches and 0.300 inches

**Body Height:** 

Between 0.120 inches and 0.195 inches

**Maximum Power Dissipation Rating:** 

512.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### **Storage Tempurature Range:**

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and monolithic and positive outputs and schottky and 3-state output and w/decoded output and programmable and programmed and w/enable and w/disable

#### **Inclosure Material:**

Ceramic and glass

## **Inclosure Configuration:**

Dual-in-line

# **Output Logic Form:**

Transistor-transistor logic

# **Input Circuit Pattern:**

6 input

#### **Case Outline Source And Designator:**

-0-001-ag joint electron device engineering council

# **Terminal Surface Treatment:**

Solder

# Voltage Rating And Type Per Characteristic:

5.5 volts power source

## **Time Rating Per Chacteristic:**

30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level output

#### **Memory Device Type:**

Rom

# **Memory Capacity:**

Unknown

## **Test Data Document:**

26916-058-002131 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

#### **Terminal Type And Quantity:**

16 flat leads

#### Shelf Life:

N/a

# **Unit Of Measure:**

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Demilitarization:

Yes - demil/mli

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