NSN 5962-01-079-7835

Memory Microcircuit - Page 1 of 1



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Body Length:

1.090 inches

Body Width:

0.450 inches

Body Height:

Between 0.050 inches and 0.130 inches

Maximum Power Dissipation Rating:

520.0 milliwatts

Operating Tempurature Range:

+0.0/+70.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and monolithic and negative outputs and positive outputs and w/enable and w/buffered output and w/decoded output

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Dual-in-line

Output Logic Form:

N-type metal oxide-semiconductor logic

Input Circuit Pattern:

14 input

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

30.0 volts power source

Time Rating Per Chacteristic:

180.00 nanoseconds propagation delay time, low to high level output and 180.00 nanoseconds propagation delay time, high to low level output

Memory Device Type:

Ram

Test Data Document:

97942-581r888 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

22 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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