NSN 5962-01-081-3591

Memory Microcircuit - Page 1 of 1



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Body Length:

Between 0.755 inches and 0.785 inches

Body Width:

0.280 inches

Body Height:

0.180 inches

Maximum Power Dissipation Rating:

500.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Features Provided:

Programmable and 3-state output

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

6 input

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

5.2 volts power source

Time Rating Per Chacteristic:

40.00 nanoseconds propagation delay time, low to high level output

Memory Device Type:

Rom

Test Data Document:

80063-sm-a-699714 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

16 printed circuit

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

A458a0