NSN 5962-01-082-1492

Memory Microcircuit - Page 1 of 1



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Features Provided:

Programmed and bipolar and monolithic

Inclosure Configuration:

Flat pack

Memory Device Type:

Prom

Test Data Document:

99971-7543607 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;

excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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