## NSN 5962-01-082-8447

Linear Microcircuit - Page 1 of 1



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9.425 indes         9.635 indes         9.635 indes         9.635 indes         9.635 indes         9.636 indes         9.636 indes         9.636 indes         9.636 indes         9.637 indes         9.637 indes         9.637 indes         9.637 indes index         9.637 index index         9.647 index         9.647 index </th <th>Body Length:</th> <th></th>	Body Length:	
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Body Height:         0.10 followinds         00.00 millowands         00.00 millowands         00.00 millowands         0.50 with 20 degrees celsis         Start 20 degrees celsis         50.00 millowands         6.50 with 20 degrees celsis         Factor 200 degregrees celsis <td>Body Width:</td> <td></td>	Body Width:	
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Asimum Power Dissipation Rating:         9000 millivatits         Operating Tempurature Range:         650/41500 diognees colisius         Boding Tempurature Range:         650/41500 diognees colisius         Participe Rating Ra	Body Height:	
900. millivatis         900. millivatis         950.01.01.01.01.01.01.01.01.01.01.01.01.01	0.130 inches	
Operating Tempurature Range:         -55.0/+125.0 degrees celsius         Storage Tempurature Range:         -65.0/+125.0 degrees celsius         Botage Tempurature Range:         -65.0/+150.0 degrees celsius         Botage Tempurature Range:         -64.0 Application:         Radar set, type an/app-1         Facture Provided:         Inclosure Material         Ceramic and glass         Inclosure Configuration:         Inclosure Configuration:         Input Circuit Pattern:         1 anput         Ontegree Temperature Rate Reserved         1 anput         Contiguration:         1 anput         Contiguration:         1 anput         2 anp	Maximum Power D	issipation Rating:
S0/+125.0 degrees celsius         Storage Tempurature Range:         -56.0/+150.0 degrees celsius         -56.0/+150.0 degrees celsius         End Application:         Radar set, type an/apy-1         Factures Provided:         Provided and hermetically sealed and positive outputs         Inclusure Material:         Coranic and glass         Coranic and glass         Inclusure Configuration:         Fat pack         Dargin Function And Quantity:         1 driver         Design Function And Quantity:         1 driver         Colored Rating And Type Per Characteristic:         0 volts prover source         Time Atta Decument:         2 drade and possition delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanos	900.0 milliwatts	
Slorage Tempurature Range: -550/+1500 degrees celsius -550/+1500 degrees celsius End Application: Radar set, type andapy-1 Features Provided: Robure Provided: Robure Provided: Robure Configuration:	Operating Tempura	ature Range:
Bab.94150.0 degrees celsius         End Application:         Radars type an/apy-1         Features Provided:         Hydid and hemstically sealed and positive outputs         Inclosure Material:         Caranic and glass         Inclosure Configuration:         Bat pack         Hort Could Pattern:         4 input         Disciption And Quantity:         1 driver         Solder         Voltage Rating And Type Per Characteristic:         0 solver source         Time Rating Per Characteristic:         0 solver source         Tarting Surface Treatment:         0 solver source         Time Rating Per Characteristic:         0 solver source	-55.0/+125.0 degree	es celsius
not Application:         Radar sat, type an/apy-1         Features Provided:         Hybrid and hermetically sealed and positive outputs         Inclosure Material:         Ceramic and glass         Inclosure Configuration:         Flat pack         Input Circuit Pattern:         4 input         Design function And Quantity:         1 driver         2 driver         Solder         Votage Rating And Type Per Characteristic:         0 ovolus power source         Time Rating Per Characteristic:         0 ovolus power source         Test Data Document:         97942 Selfride Graving (this is the basic governing draving, such as a contractor draving, original equipment manufacturer draving, excluses any specification, standard or other document that may be referenced in a basic governing draving)         Votage Selfride Graving (this is the basic governing draving, such as a contractor draving, original equipment manufacturer draving, excluses any specification, standard or other document that may be referenced in a basic governing draving)         Votage Rating And Type And Quantity:         2 of tal leads         Sheft Life:         Na         Data Interimetication, standard or other document that may be referenced in a basic governing draving)         Votage Rating And Type Per Characteristic:	Storage Tempuratu	ire Range:
Radar set, type an/apy-1         Fectures Provided:         Hybrid and hermetically sealed and positive outputs         Inclosure Configuration:         Ceramic and glass         Inclosure Configuration:         Flat pack         Position And Quantity:         4 input         Design Function And Quantity:         1 driver         Solder         Votage Rating And Type Per Characteristic:         0.0 volks power source         100.0 volks power source         Votage Rating And Type Per Characteristic:         0.0 volks power source         100.0 volks power source         100.0 volks power source         100.0 volks power source         100.0 volks power source         101.0 volks power source         102.0 volks power source         103.0 volks power source         103.0 volks power source         103.0 volks power source         104.0 volks         105.0 volks power source	-65.0/+150.0 degree	es celsius
Fedures Provided:         Hybrid and hermetically sealed and positive outputs         Inclosure Material:         Corsure Material:         Corsure Configuration:         Fila pack         Inclosure Configuration:         Fair pack         Daty Circuit Pattern:         4 input         Design Function And Quantity:         1 driver         2 driver         Voltage Rating And Type Per Characteristic:         0 drover source         2 drover source         2 motion anoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagating delay time, low to high level output and 30.	End Application:	
Hybrid and hermetically sealed and positive outputs         Inclosure Material:         Ceramic and glass         Inclosure Configuration:         Flat pack         Input Circuit Pattern:         A input         Design Function And Quantity:         1 driver         Solder         Voltage Rating And Type Per Characteristic:         0.0 volts power source         Tom Rating Per Chateristic:         0.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagating dup and 30.00 nanoseconds propagation de	Radar set, type an/a	py-1
Inclosure Material:   Ceramic and glass   Inclosure Configuration:   Flat pack   Input Circuit Pattern:   4 input   Design Function And Quantity:   1 driver   Terminal Surface Treatment:   Solder   Voltage Rating And Type Per Characteristic:   0.0 volts power source   Time Rating Per Chacteristic:   30.0 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level   output   Test Dab Cournent:   97942-581r856 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;   excludes any specification, standard or other document that may be referenced in a basic governing drawing, etc.;   excludes any specification, standard or other document that may be referenced in a basic governing drawing, etc.;   excludes any specification, standard or other document that may be referenced in a basic governing drawing, etc.;   excludes any specification, standard or other document that may be referenced in a basic governing drawing.   Terminal Type And Quantity:   20 flat leads   Shell Life:   N/a   Init of Measure:	Features Provided	
Ceramic and glass  Inclosure Configuration: Flat pack  Flat pack  Input Circuit Pattern: 4 input  Obsign Function And Quantity: 1 driver  Tornal Surface Treatment: Solder  Voltage Rating And Type Per Characteristic: 0.0 volts power source  Torne Rating Per Characteristic: 0.0 volts power source  Tote Sate Decoment: 97942-5811856 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc. excludes any specification, standard or other document that may be referenced in a basic governing drawing, etc. 8 Sheft Life: 10 June Characteristic: 10	Hybrid and hermetic	ally sealed and positive outputs
Iclosure Configuration:   Flat pack   Input Circuit Pattern:   4 input   Design Function And Quantity:   1 driver   Ternial Surface Treatment:   Solder   Voltage Rating And Type Per Characteristic:   0.0 volts power source   Time Rating Per Characteristic:   0.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds	Inclosure Material:	
Flat pack         I pupt Circuit Pattern:         4 input         Design Function And Quantity:         1 driver         Terminal Surface Treatment:         Solder         Voltage Rating And Type Per Characteristic:         0.0 volts power source         Time Rating Per Chateristic:         0.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagatin delay time, low	Ceramic and glass	
Purc Circuit Pattern:         4 input         Design Function And Quantity:         1 driver         Terminal Surface Treatment:         Solder         Voltage Rating And Type Per Characteristic:         0.0 volts power source         Time Rating Per Chacteristic:         0.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds	Inclosure Configur	ation:
4 input         Design Function And Quantity:         1 driver         Terminal Surface Treatment:         Solder         Voltage Rating And Type Per Characteristic:         0.0 volts power source         Time Rating Per Chacteristic:         30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level output         output         Test Data Document:         97942-581r856 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)         Terminal Type And Quantity:         20 flat leads         Shelf Life:         Na         Int Of Measure:               Demilitrization:         Yes - demil/mli	Flat pack	
Perign Function And Quantity:         1 driver         Enrinal Surface Treatment:         Solder         Voltage Rating And Type Per Characteristic:         0.0 volts power source         Time Rating Per Chacteristic:         0.00 annoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level         0 uput         Test Data Document:         97942-581r856 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;         excludes any specification, standard or other document that may be referenced in a basic governing drawing).         Terninal Type And Quantity:         20 flat leads         Shelf Life:         Na         Dit Of Measure:               Penilitarization:         Yes - demil/mli	Input Circuit Patter	'n:
1 virver         Erminal Surface Treatment:         Solder         Voltage Rating And Type Per Characteristic:         0.0 volts power source         Time Rating Per Chacteristic:         30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level         output         Test Data Document:         97942-581r866 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;         exclues any specification, standard or other document that may be referenced in a basic governing drawing)         Terminal Type And Quantity:         20 flat leads         Shelf Life:         N/a         Unt Of Measure:            Demilitrization:         Yes - demil/mli         Fig:	4 input	
Terminal Surface Treatment:         Solder         Voltage Rating And Type Per Characteristic:         0.0 volts power source         Time Rating Per Chacteristic:         3000 annoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level         0 output         Test Data Document:         97942-581r856 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;         excludes any specification, standard or other document that may be referenced in a basic governing drawing)         Terminal Type And Quantity:         20 flat leads         Shelf Life:         Na         Uht Of Measure:         -         peniltarization:         Yes - demil/mli         Fig:	Design Function A	nd Quantity:
Solder Voltage Rating And Type Per Characteristic: 0.0 volts power source Time Rating Per Chacteristic: 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level output Test Data Document: 97942-581r856 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing). Terminal Type And Quantity: 20 flat leads Pael Life: Na Other Measure:	1 driver	
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0.0 volts power source Time Rating Per Chacteristic: 30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level output Test Data Document: 97942-581r856 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing) Terminal Type And Quantity: 20 flat leads Shelf Life: N/a Unit Of Measure:  Demilitarization: Yes - demil/mli Fig:	Solder	
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Test Data Document: 97942-581r856 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing) Terminal Type And Quantity: 20 flat leads Shelf Life: N/a Unit Of Measure:  Demilitarization: Yes - demil/mli Fig:	30.00 nanoseconds	propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level
97942-581r856 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing) <b>Terminal Type And Quantity:</b> 20 flat leads <b>Shelf Life:</b> N/a <b>Unit Of Measure:</b>  <b>Demilitarization:</b> Yes - demil/mli <b>Fig:</b>	output	
excludes any specification, standard or other document that may be referenced in a basic governing drawing) Terminal Type And Quantity: 20 flat leads Shelf Life: N/a V/a Unit Of Measure: Demilitarization: Yes - demil/mli Fig:	Test Data Docume	nt:
Terminal Type And Quantity:   20 flat leads   Shelf Life:   N/a   Unit Of Measure:     Demilitarization:   Yes - demil/mli   Fig:	97942-581r856 drav	ving (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;
20 flat leads Shelf Life: N/a Unit Of Measure: Demilitarization: Yes - demil/mli Fig:	excludes any specifi	cation, standard or other document that may be referenced in a basic governing drawing)
Shelf Life:   N/a   Unit Of Measure:     Demilitarization:   Yes - demil/mli   Fiig:	Terminal Type And	Quantity:
N/a Unit Of Measure:  Demilitarization: Yes - demil/mli Fiig:	20 flat leads	
Unit Of Measure:  Demilitarization: Yes - demil/mli Fiig:	Shelf Life:	
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	Yes - demil/mli	
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