NSN 5962-01-085-4253

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Body Length:

Between 1.010 inches and 1.050 inches

Body Width:

Between 0.250 inches and 0.290 inches

Body Height:

Between 0.135 inches and 0.140 inches

Operating Tempurature Range:

-0.0/+70.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Schottky and monolithic and positive outputs and 3-state output and w/enable

Inclosure Material:

Plastic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

10 input

Design Function And Quantity:

8 buffer, noninverting

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

7.0 volts power source

Time Rating Per Chacteristic:

9.00 nanoseconds propagation delay time, low to high level output and 9.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

26916-058-002144 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,

etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

20 printed circuit

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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