# NSN 5962-01-089-2051

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### **Body Length:**

Between 0.935 inches and 0.970 inches

### **Body Width:**

Between 0.245 inches and 0.285 inches

### **Body Height:**

Between 0.125 inches and 0.160 inches

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and monolithic and positive outputs and w/enable and low power and schottky and synchronous and 3-state output

#### **Inclosure Material:**

Ceramic and glass

#### Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

9 input

## **Design Function And Quantity:**

1 shift register, parallel and/or series operation

## **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

5.5 volts power source

#### **Test Data Document:**

12115-sc5962-0345 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

#### Terminal Type And Quantity:

20 printed circuit

#### Shelf Life:

N/a

#### Unit Of Measure:

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#### Demilitarization:

Yes - demil/mli

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