NSN 5962-01-093-8143

Memory Microcircuit - Page 1 of 1



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Body Length:
Between 0.735 inches and 0.830 inches
Body Width:
Between 0.265 inches and 0.291 inches
Body Height:
Between 0.150 inches and 0.180 inches
Operating Tempurature Range:
+0.0/+70.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic and positive outputs and bipolar and programmable and w/decoded output and 3-state output
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
10 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.5 volts power source
Time Rating Per Chacteristic:
90.00 nanoseconds propagation delay time, low to high level output and 90.00 nanoseconds propagation delay time, high to low leve
output
Memory Device Type:
Rom
Memory Capacity:
Unknown
Test Data Document:
08783-37658031 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing
etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
16 printed circuit
Shelf Life:
N/a
Unit Of Measure:

Fiig:

A458a0

Demilitarization: Yes - demil/mli