NSN 5962-01-093-8819

Memory Microcircuit - Page 1 of 1



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| Body Length: |
|--|
| 0.808 inches |
| Body Width: |
| 0.294 inches |
| Body Height: |
| 0.179 inches |
| Maximum Power Dissipation Rating: |
| 800.0 milliwatts |
| Operating Tempurature Range: |
| -55.0/+125.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+150.0 degrees celsius |
| Features Provided: |
| Bipolar and hermetically sealed and monolithic and 3-state output and w/enable and programmable and programmed and high speed |
| Inclosure Material: |
| Ceramic |
| Inclosure Configuration: |
| Dual-in-line |
| Output Logic Form: |
| Transistor-transistor logic |
| Input Circuit Pattern: |
| 6 input |
| Terminal Surface Treatment: |
| Solder |
| Voltage Rating And Type Per Characteristic: |
| 5.5 volts power source |
| Memory Device Type: |
| Rom |
| Memory Capacity: |
| Unknown |
| Test Data Document: |
| 97942-581r137 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; |
| excludes any specification, standard or other document that may be referenced in a basic governing drawing) |
| Terminal Type And Quantity: |
| 16 printed circuit |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |
| Yes - demil/mli |
| Fiig: |
| A458a0 |
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