NSN 5962-01-094-9810

Digital Microcircuit - Page 1 of 1



View Online at https://aerobasegroup.com/nsn/5962-01-094-9810
Body Length:
Between 0.660 inches and 0.785 inches
Body Width:
Between 0.220 inches and 0.280 inches
Body Height:
Between 0.140 inches and 0.180 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and positive outputs and w/enable and edge triggered
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Dual 4 input
Design Function And Quantity:
2 flip-flop, j-k, master slave
Case Outline Source And Designator:
T0-116 joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-0.5 volts power source and 5.5 volts power source
Time Rating Per Chacteristic:
50.00 nanoseconds propagation delay time, low to high level output and 50.00 nanoseconds propagation delay time, high to low level
output
Test Data Document:
$06481-970086 \ drawing, \ this is the \ basic \ governing \ drawing, \ such \ as \ a \ contractor \ drawing, \ original \ equipment \ manufacturer \ drawing, \ etc.;$
excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
14 printed circuit
Shelf Life:
N/a
Unit Of Measure:

Fiig: A458a0

Demilitarization: Yes - demil/mli