NSN 5962-01-096-5760

Digital Microcircuit - Page 1 of 1



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Body Length:

0.350 inches

Body Width:

0.265 inches

Body Height:

Between 0.030 inches and 0.080 inches

Maximum Power Dissipation Rating:

15.0 milliwatts

Operating Tempurature Range:

-0.0/+70.0 degrees celsius

Storage Tempurature Range:

-65.0/+200.0 degrees celsius

Features Provided:

Electrostatic sensitive

Inclosure Material:

Plastic

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Quad 2 input

Design Function And Quantity:

4 gate, nand-nor

Time Rating Per Chacteristic:

15.00 nanoseconds propagation delay time, low to high level output and 15.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

05869-717315-17 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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