# NSN 5962-01-097-2455

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# **Body Length:**

Between 0.755 inches and 0.785 inches

# Body Width:

Between 0.245 inches and 0.280 inches

# **Body Height:**

0.180 inches

# **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **Features Provided:**

Hermetically sealed and monolithic and low power and schottky

#### **Inclosure Material:**

Ceramic and glass

#### Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

Quad 2 input

# **Design Function And Quantity:**

4 gate, nand

## Time Rating Per Chacteristic:

15.00 nanoseconds propagation delay time, low to high level output and 15.00 nanoseconds propagation delay time, high to low level output

#### Shelf Life:

N/a

## Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

## Fiig:

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