# NSN 5962-01-099-8208

Memory Microcircuit - Page 1 of 1



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## **Body Length:**

Between 0.882 inches and 0.925 inches

## **Body Width:**

0.310 inches

# **Body Height:**

Between 0.145 inches and 0.175 inches

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **Features Provided:**

Hermetically sealed and monolithic and positive outputs and w/enable and w/open collector and 3-state output

#### **Inclosure Material:**

Ceramic and glass

#### Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

12 input

**Terminal Surface Treatment:** 

Solder

## Voltage Rating And Type Per Characteristic:

5.5 volts power source

## Time Rating Per Chacteristic:

40.00 nanoseconds propagation delay time, low to high level output and 40.00 nanoseconds propagation delay time, high to low level output

## Memory Device Type:

Rom

## Memory Capacity:

Unknown

## Test Data Document:

11627-21008 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

## **Terminal Type And Quantity:**

18 printed circuit

Shelf Life:

N/a

## Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

# Fiig:

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