# NSN 5962-01-100-1762

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# **Body Length:**

Between 0.660 inches and 0.785 inches

# **Body Width:**

Between 0.220 inches and 0.280 inches

# **Body Height:**

Between 0.140 inches and 0.180 inches

# **Maximum Power Dissipation Rating:**

10.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and monolithic and positive outputs

#### **Inclosure Material:**

# Ceramic and glass

# **Inclosure Configuration:**

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

8 input

#### **Design Function And Quantity:**

1 gate, nand-nor

#### Case Outline Source And Designator:

T0-116 joint electron device engineering council

#### **Terminal Surface Treatment:**

Gold

#### Voltage Rating And Type Per Characteristic:

5.5 volts power source

#### Time Rating Per Chacteristic:

22.00 nanoseconds propagation delay time, low to high level output and 15.00 nanoseconds propagation delay time, high to low level

# output

#### **Precious Material And Location:**

Terminal surface gold

#### **Precious Material:**

Gold

# **Terminal Type And Quantity:**

14 printed circuit

#### Shelf Life:

N/a

# Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

Fiig:

A458a0

