NSN 5962-01-102-5312

Digital Microcircuit - Page 1 of 1



View Online at https://aerobasegroup.com/nsn/5962-01-102-5312 **Body Length:** Between 0.660 inches and 0.785 inches **Body Width:** Between 0.220 inches and 0.280 inches **Body Height:** 0.180 inches **Operating Tempurature Range:** -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius **Features Provided:** Hermetically sealed and monolithic and schottky and w/totem pole output **Inclosure Material:** Ceramic **Inclosure Configuration:** Dual-in-line **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** Quad 2 input **Design Function And Quantity:**

4 gate, and

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

5.5 volts power source

Time Rating Per Chacteristic:

7.00 nanoseconds propagation delay time, low to high level output and 7.50 nanoseconds propagation delay time, high to low level output

Test Data Document:

96214-802412 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

14 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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