NSN 5962-01-102-9533

Memory Microcircuit - Page 1 of 1

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Body Length:
Between 0.710 inches and 0.770 inches
Body Width:
Between 0.240 inches and 0.260 inches
Body Height:
0.180 inches
Operating Tempurature Range:
-0.0/+70.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Monolithic
Inclosure Material:
Plastic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
6 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
7.0 volts power source
Time Rating Per Chacteristic:
48.00 nanoseconds propagation delay time, low to high level output and 50.00 nanoseconds propagation delay time, high to low level
output
Memory Device Type:
Rom
Test Data Document:
89536-293159 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification format
excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental
and performance requirements and test conditions that are shown as "typical", "average", "", etc.).
Terminal Type And Quantity:
14 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig: