NSN 5962-01-104-7272

Memory Microcircuit - Page 1 of 1



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Body Length:
Between 0.755 inches and 0.790 inches
Body Width:
Between 0.265 inches and 0.300 inches
Body Height:
Between 0.155 inches and 0.175 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Bipolar and hermetically sealed and monolithic and positive outputs and programmable and w/enable and 3-state output
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
6 input
Case Outline Source And Designator:
D-2 mil-m-38510
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.5 volts power source
Time Rating Per Chacteristic:
30.00 nanoseconds propagation delay time, low to high level output and 25.00 nanoseconds propagation delay time, high to low leve
output
Memory Device Type:
Rom
Memory Capacity:
Unknown
Terminal Type And Quantity:
16 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:

Fiig:

A458a0

Yes - demil/mli