NSN 5962-01-104-7718

Linear Microcircuit - Page 1 of 1



low to high level output and 1000.00 nanoseconds propagation delay time, high to low level

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| Body Length: |
|--|
| 0.280 inches |
| Body Width: |
| Between 0.240 inches and 0.260 inches |
| Body Height: |
| Between 0.030 inches and 0.085 inches |
| Maximum Power Dissipation Rating: |
| 750.0 milliwatts |
| Operating Tempurature Range: |
| -55.0/+125.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+150.0 degrees celsius |
| Features Provided: |
| Hermetically sealed and monolithic and w/field effect transistor switch |
| Inclosure Material: |
| Ceramic |
| Inclosure Configuration: |
| Flat pack |
| Input Circuit Pattern: |
| 2 channel |
| Design Function And Quantity: |
| 2 switch, analog and 2 driver |
| Case Outline Source And Designator: |
| F-1 mil-m-38510 |
| Terminal Surface Treatment: |
| Solder |
| Voltage Rating And Type Per Characteristic: |
| 6.0 volts power source |
| Time Rating Per Chacteristic: |
| 300.00 nanoseconds propagation delay time, low to high level output and 1000.00 nanoseconds propagation delay |
| output |
| Test Data Document: |
| 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.). |
| Terminal Type And Quantity: |
| 14 flat leads |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |
| Yes - demil/mli |
| Filg: |
| |

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