## NSN 5962-01-104-8743

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| Body Length:  |
|---|
| Between 0.755 inches and 0.785 inches   |
| Body Width:   |
| Between 0.245 inches and 0.280 inches   |
| Body Height:  |
| 0.180 inches  |
| Maximum Power Dissipation Rating:   |
| 90.0 milliwatts   |
| Operating Tempurature Range:  |
| -55.0/+125.0 degrees celsius  |
| Storage Tempurature Range:  |
| -65.0/+150.0 degrees celsius  |
| Features Provided:  |
| Hermetically sealed and monolithic and resettable and positive outputs  |
| Inclosure Material:   |
| Ceramic and glass   |
| Inclosure Configuration:  |
| Dual-in-line  |
| Output Logic Form:  |
| Transistor-transistor logic   |
| Design Function And Quantity:   |
| 4 latch, nand, r/s  |
| Terminal Surface Treatment:   |
| Solder  |
| Voltage Rating And Type Per Characteristic:   |
| 5.5 volts power source  |
| Time Rating Per Chacteristic:   |
| 22.00 nanoseconds propagation delay time, low to high level output and 27.00 nanoseconds propagation delay time, high to low leve |
| output  |
| Test Data Document:   |
| 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).                |
| Terminal Type And Quantity:   |
| 16 printed circuit  |
| Shelf Life:   |
| N/a   |
| Unit Of Measure:  |
|   |
| Demilitarization:   |
| Yes - demil/mli   |
|   |

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