## NSN 5962-01-106-8172

Memory Microcircuit - Page 1 of 1



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|--|
| Body Length:   |
| 0.870 inches   |
| Body Width:  |
| 0.260 inches   |
| Body Height:   |
| 0.180 inches   |
| Operating Tempurature Range:                                       |
| +0.0/+70.0 degrees celsius   |
| Storage Tempurature Range:   |
| -65.0/+150.0 degrees celsius                                       |
| Features Provided:   |
| Monolithic and positive outputs and programmable and programmed    |
| Inclosure Material:  |
| Plastic  |
| Inclosure Configuration:   |
| Dual-in-line   |
| Output Logic Form:   |
| Transistor-transistor logic  |
| Input Circuit Pattern:   |
| 6 input  |
| Terminal Surface Treatment:  |
| Solder   |
| Voltage Rating And Type Per Characteristic:                        |
| 5.5 volts power source   |
| Time Rating Per Chacteristic:                                      |
| 50.00 nanoseconds propagation delay time, low to high level output |
| Memory Device Type:  |
| Rom  |
| Memory Capacity:   |
| Unknown  |
| Terminal Type And Quantity:  |
| 16 printed circuit   |
| Shelf Life:  |
| N/a  |
| Unit Of Measure:   |
|  |
| Demilitarization:  |
| Yes - demil/mli  |
| Fiig:  |
| A458a0   |
|  |