## NSN 5962-01-107-8436

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| Body Length:   |
|--|
| 1.290 inches   |
| Body Width:  |
| Between 0.500 inches and 0.610 inches  |
| Body Height:   |
| Between 0.150 inches and 0.210 inches  |
| Operating Tempurature Range:   |
| -55.0/+125.0 degrees celsius   |
| Storage Tempurature Range:   |
| -65.0/+150.0 degrees celsius   |
| Features Provided:   |
| Hermetically sealed and monolithic and w/clear and w/enable  |
| Inclosure Material:  |
| Ceramic  |
| Inclosure Configuration:   |
| Dual-in-line   |
| Output Logic Form:   |
| Transistor-transistor logic  |
| Input Circuit Pattern:   |
| 14 input   |
| Design Function And Quantity:  |
| 1 shift register, bidirectional  |
| Case Outline Source And Designator:  |
| D-3 mil-m-38510  |
| Terminal Surface Treatment:  |
| Solder   |
| Voltage Rating And Type Per Characteristic:  |
| -1.5 volts power source and 5.5 volts power source   |
| Test Data Document:  |
| 19397-67150 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification format; |
| excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental      |
| and performance requirements and test conditions that are shown as "typical", "average", "", etc.).  |
| Terminal Type And Quantity:  |
| 24 printed circuit   |
| Shelf Life:  |
| N/a  |
| Unit Of Measure:   |
|  |
| Demilitarization:  |
| Yes - demil/mli  |
| Fiig:  |
| A458a0   |