NSN 5962-01-108-7156

Digital Microcircuit - Page 1 of 1



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Body L	ength:
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Between 0.750 inches and 0.785 inches

Body Width:

Between 0.240 inches and 0.295 inches

Body Height:

Between 0.160 inches and 0.180 inches

Maximum Power Dissipation Rating:

15.0 milliwatts

Operating Tempurature Range:

-40.0/+85.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and monolithic and low power

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Complementary-metal oxide-semiconductor logic

Input Circuit Pattern:

Quad 2 input

Design Function And Quantity:

4 gate, nor

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

16.0 volts power source

Time Rating Per Chacteristic:

100.00 nanoseconds propagation delay time, low to high level output and 100.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

80103-fbt-00-044 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.).

Terminal Type And Quantity:

14 printed circuit

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

A 450a0