## NSN 5962-01-108-7866

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Body Length:
Between 0.755 inches and 0.785 inches
Body Width:
Between 0.265 inches and 0.300 inches
Body Height:
Between 0.155 inches and 0.175 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic and positive outputs and programmable and schottky and w/decoded output and 3-state output
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
10 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.5 volts power source
Time Rating Per Chacteristic:
25.00 nanoseconds propagation delay time, low to high level output and 25.00 nanoseconds propagation delay time, high to low level
output
Memory Device Type:
Rom
Test Data Document:
13499-351-7769 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;
excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
16 printed circuit
Shelf Life:
N/a
Unit Of Measure:
<del></del>
Demilitarization:
Yes - demil/mli

Fiig:

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