

View Online at <https://aerobasegroup.com/nsn/5962-01-108-8168>

Features Provided:

Monolithic and high speed

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Quad 2 input

Test Data Document:

53178-04040008 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fig:

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