NSN 5962-01-108-8785

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Body Length:
Between 0.745 inches and 0.770 inches
Body Width:
Between 0.240 inches and 0.260 inches
Body Height:
Between 0.105 inches and 0.180 inches
Maximum Power Dissipation Rating:
500.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and complementary outputs and high voltage
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Complementary-metal oxide-semiconductor logic
Input Circuit Pattern:
Quad 2 input
Design Function And Quantity:
4 gate, nor
Case Outline Source And Designator:
Mo-001-ab joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
20.5 volts power source
Time Rating Per Chacteristic:
120.00 nanoseconds propagation delay time, low to high level output and 120.00 nanoseconds propagation delay time, high to low level
output
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
14 printed circuit
Shelf Life:
N/a

Demilitarization:

Unit Of Measure:

Yes - demil/mli

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