

View Online at <https://aerobasegroup.com/nsn/5962-01-111-7471>

**Features Provided:**

Monolithic and programmable and 3-state output

**Inclosure Configuration:**

Dual-in-line

**Output Logic Form:**

Transistor-transistor logic

**Input Circuit Pattern:**

9 input

**Terminal Surface Treatment:**

Solder

**Time Rating Per Characteristic:**

70.00 nanoseconds propagation delay time, low to high level output

**Memory Device Type:**

Rom

**Test Data Document:**

05869-726009-1 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

**Terminal Type And Quantity:**

16 printed circuit

**Shelf Life:**

N/a

**Unit Of Measure:**

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**Demilitarization:**

Yes - demil/mli

**Fig:**

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