## NSN 5962-01-111-7858

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## **Body Length:** 0.840 inches **Body Width:** Between 0.220 inches and 0.310 inches **Body Height:** Between 0.140 inches and 0.185 inches **Maximum Power Dissipation Rating:** 209.0 milliwatts **Operating Tempurature Range:** -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius **Features Provided:** Hermetically sealed and monolithic and low power and schottky and w/clear and w/enable **Inclosure Material:** Ceramic **Inclosure Configuration:** Dual-in-line **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** 13 input **Design Function And Quantity:** 1 shift register, parallel in, parallel out **Case Outline Source And Designator:** D-2 mil-m-38510 **Terminal Surface Treatment:** Solder Voltage Rating And Type Per Characteristic: 5.5 volts power source **Time Rating Per Chacteristic:** 52.00 nanoseconds propagation delay time, low to high level output and 52.00 nanoseconds propagation delay time, high to low level output **Test Data Document:** 14933-80017 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing) **Terminal Type And Quantity:** 16 printed circuit Shelf Life: N/a Unit Of Measure: **Demilitarization:**

Yes - demil/mli

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