NSN 5962-01-111-7861

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Operating Tempurature Range:
-0.0/+80.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic and high speed
nclosure Material:
Ceramic
nclosure Configuration:
Dual-in-line
Output Logic Form:
Fransistor-transistor logic
nput Circuit Pattern:
Quad 2 input
Design Function And Quantity:
I gate, and
Test Data Document:
05869-717128-509 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,
etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Shelf Life:
N/a
Jnit Of Measure:
-
Demilitarization:
es - demil/mli
Filg:
A458a0