## NSN 5962-01-111-9363

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Operating Tempurature Range:
-0.0/+70.0 degrees celsius
Features Provided:
Monolithic and programmable and hermetically sealed
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
8 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
7.0 volts power source
Memory Device Type:
Rom
Test Data Document:
00752-503251 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;
excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
18 printed circuit
Shelf Life:
N/a
Unit Of Measure:
<del></del>
Demilitarization:
Yes - demil/mli
Fiig:
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