NSN 5962-01-112-1018

Memory Microcircuit - Page 1 of 1



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Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
12 input
Criticality Code Justification:
Feat
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.5 volts power source
Memory Device Type:
Rom
Memory Capacity:
4096 bit
Special Features:
Static electric sensitive
Terminal Type And Quantity:
18 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
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