NSN 5962-01-112-1021

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Maximum Power Dissipation Rating:
90.0 milliwatts
Operating Tempurature Range:
-0.0/+80.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic and high speed
Inclosure Material:
Ceramic
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Quad 2 input
Design Function And Quantity:
4 gate, nand
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.5 volts power source
Time Rating Per Chacteristic:
10.00 nanoseconds propagation delay time, low to high level output and 10.00 nanoseconds propagation delay time, high to low level
output
Test Data Document:
05869-717129-501 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,
etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
14 flat leads
Shelf Life:
N/a
Unit Of Measure:
-
Demilitarization:
Yes - demil/mli
Fiig:
A458a0