# NSN 5962-01-112-1026

Memory Microcircuit - Page 1 of 1



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## **Body Length:**

1.310 inches

#### **Body Width:**

0.550 inches

# **Body Height:**

0.180 inches

#### **Operating Tempurature Range:**

+0.0/+75.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **Features Provided:**

Hermetically sealed and monolithic and positive outputs and bipolar and w/enable and programmable

#### **Inclosure Material:**

Ceramic and glass

#### Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

14 input

**Terminal Surface Treatment:** 

Solder

## Voltage Rating And Type Per Characteristic:

5.5 volts power source

## **Time Rating Per Chacteristic:**

100.00 nanoseconds propagation delay time, low to high level output and 100.00 nanoseconds propagation delay time, high to low level output

#### Memory Device Type:

Rom

## Test Data Document:

36378-1003801 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

## **Terminal Type And Quantity:**

24 printed circuit

#### Shelf Life:

N/a

## Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

# Fiig:

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