# NSN 5962-01-112-6360

Memory Microcircuit - Page 1 of 1



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# **Body Length:**

Between 1.240 inches and 1.280 inches

# **Body Width:**

0.530 inches

# **Body Height:**

0.170 inches

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and monolithic and positive outputs and bipolar and programmable and w/decoded output and w/enable and expandable

#### **Inclosure Material:**

Ceramic and glass

#### Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

8 input

# **Terminal Surface Treatment:**

Solder

# Voltage Rating And Type Per Characteristic:

5.5 volts power source

# Time Rating Per Chacteristic:

55.00 nanoseconds propagation delay time, low to high level output and 55.00 nanoseconds propagation delay time, high to low level output

#### Memory Device Type:

Rom

#### **Memory Capacity:**

Unknown

#### **Test Data Document:**

81755-654+5501 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,

etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

## **Terminal Type And Quantity:**

24 printed circuit

#### Shelf Life:

N/a

# Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

# Fiig:

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