NSN 5962-01-113-2799

Memory Microcircuit - Page 1 of 1

Fiig: A458a0



| View Online at https://aerobasegroup.com/nsn/5962-01-113-2799 |
|-----------------------------------------------------------------------------------------------------------------------------------------|
| Operating Tempurature Range: |
| -55.0/+125.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+150.0 degrees celsius |
| Features Provided: |
| Asynchronous and synchronous and bipolar and bistable and burn in and complementary outputs and hermetically sealed and high speed |
| and monolithic and positive edge triggered and positive outputs and w/clear and w/clock and w/preset and w/independent outputs and |
| w/totem pole output and 2-state output |
| Inclosure Material: |
| Ceramic |
| Inclosure Configuration: |
| Dual-in-line |
| Output Logic Form: |
| Transistor-transistor logic |
| Input Circuit Pattern: |
| Dual 5 input |
| Terminal Surface Treatment: |
| Solder |
| Voltage Rating And Type Per Characteristic: |
| 8.0 volts power source |
| Time Rating Per Chacteristic: |
| 35.00 nanoseconds propagation delay time, low to high level output and 45.00 nanoseconds propagation delay time, high to low level |
| output |
| Test Data Document: |
| 93322-7000657 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc. |
| excludes any specification, standard or other document that may be referenced in a basic governing drawing) |
| Terminal Type And Quantity: |
| 16 printed circuit |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |
| Yes - demil/mli |