NSN 5962-01-115-5560

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Body Length:

Between 0.750 inches and 0.785 inches

Body Width:

Between 0.245 inches and 0.271 inches

Body Height:

0.185 inches

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and burn in and w/enable and monolithic

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

10 input

Design Function And Quantity:

1 generator, parity, checker

Case Outline Source And Designator:

To-116 joint electron device engineering council

Time Rating Per Chacteristic:

26.00 nanoseconds propagation delay time, low to high level output and 26.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

No

Fiig:

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