# NSN 5962-01-115-8794

Memory Microcircuit - Page 1 of 1



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## **Body Length:**

Between 0.850 inches and 0.930 inches

## Body Width:

Between 0.240 inches and 0.295 inches

## **Body Height:**

0.200 inches

## **Operating Tempurature Range:**

-0.0/+70.0 degrees celsius

#### **Features Provided:**

3-state output and programmed and hermetically sealed

#### **Inclosure Material:**

Ceramic

## Inclosure Configuration:

Dual-in-line

## Output Logic Form:

#### Transistor-transistor logic

Input Circuit Pattern:

# 12 input

## **Terminal Surface Treatment:**

Solder

# Voltage Rating And Type Per Characteristic:

7.0 volts power source

# Time Rating Per Chacteristic:

70.00 nanoseconds propagation delay time, low to high level output

## Memory Device Type:

Rom

## **Terminal Type And Quantity:**

18 printed circuit

## Shelf Life:

N/a

## Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

Fiig:

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