NSN 5962-01-116-6943

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Body Leng	gth:
0.785 inche	es
Body Widt	th:
0.250 inche	es
Body Heig	Jht:
0.180 inche	es
Maximum	Power Dissipation Rating:
210.0 milliv	watts
Operating	Tempurature Range:
-55.0/+125	i.0 degrees celsius
Storage To	empurature Range:
-65.0/+150	0.0 degrees celsius
End Appli	cation:
F16maintn	trng 81755
Features F	Provided:
Hermetical	lly sealed and w/enable and complementary outputs
Inclosure	Material:
Ceramic	
Inclosure	Configuration:
Dual-in-line	e
Output Lo	gic Form:
Transistor-	transistor logic
Input Circ	uit Pattern:
12 input	
Design Fu	Inction And Quantity:
1 shift regi	ster, parallel load
Terminal S	Surface Treatment:
Solder	
Voltage Ra	ating And Type Per Characteristic:
5.5 volts p	ower source
Time Ratii	ng Per Chacteristic:
31.00 nanc	oseconds propagation delay time, low to high level output and 40.00 nanoseconds propagation delay time, high to low level
output	
Test Data	Document:
15280-48-0	050659 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,
etc.; exclu	des any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal 7	Type And Quantity:
16 printed	circuit
Shelf Life:	

Unit Of Measure:

Demilitarization:

Yes - demil/mli

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